



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Date February 24, 2003

In re the Application of

Nobuaki HASHIMOTO

Group Art Unit: 2827

Application No.: 09/720,860

Examiner: L. Thai

Filed: January 2, 2001

Docket No.: 108102

For: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURE THEREOF, CIRCUIT BOARD AND ELECTRONIC INSTRUMENT

**LARGE ENTITY PETITION FOR 1<sup>st</sup> - 3<sup>rd</sup> EXTENSION  
OF TIME UNDER 37 C.F.R. §1.136(a) AND  
TRANSMITTAL OF FEE UNDER 37 C.F.R. §1.17**

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Director of the U.S. Patent and Trademark Office  
Washington, D.C. 20231

Sir:

Transmitted herewith is a response to the outstanding Official Action in the above-identified patent application. The shortened statutory period having expired January 23, 2003, an extension of time for a period of ONE month is hereby requested. Attached hereto is our Check No. 139334 in the amount of \$110.00 in payment for:

XX Extension fee for response within first month pursuant to §1.136(a) (\$110.00)  
       Extension fee for response within second month pursuant to §1.136(a) (\$410.00)  
       Extension fee for response within third month pursuant to §1.136(a) (\$930.00)

The Director is hereby authorized to charge any additional fee or credit any overpayment associated with this communication to Deposit Account No. 15-0461. Two copies of this petition are enclosed.

Respectfully submitted,

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DEPOSIT ACCOUNT USE  
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Please grant any extension  
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**AMENDMENT UNDER 37 C.F.R. §1.111**

Director of the U.S. Patent and Trademark Office  
Washington, D.C. 20231

Sir:

In reply to the October 23, 2002 Office Action, the period for reply being extended by the attached Petition For Extension of Time, please amend the above-identified application as follows:

**IN THE CLAIMS:**

Please replace claims 1, 8, 9 and 29 as follows:

1. (Twice Amended) A semiconductor device comprising:
  - a semiconductor chip on which a plurality of electrodes are formed;
  - a first flexible substrate in a tape form on which a wiring pattern is formed and on which the semiconductor chip is mounted;
  - a plurality of external terminals electrically connected to the electrodes with the wiring pattern interposed; and
  - a second flexible substrate in a tape form adhered to the first flexible substrate by a resin avoiding the semiconductor chip,